



## COMBINED DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

As a below-named inventor, I hereby declare that:

\_\_\_\_ was filed on xxxxxx as application serial no. xxxxxx;

\_\_\_X is attached hereto;

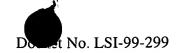
\_ was amended on xxxxxx.

My residence, post office address and citizenship are as stated below next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: RECEIVE DESERIALIZER CIRCUIT FOR FRAMING PARALLEL DATA, the specification of which

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.								
	the duty to disclose information, Code of Federal Regulations,		mination of this application in					
application(s) for patent	foreign priority benefits under T or inventor's certificate listed be certificate having a filing date be	elow and have also identifie	d below any foreign application					
PRIOR		Priority						
(Serial No.)	(Country)	(Filing Date)	Yes No					
(Serial No.)	(Country)	(Filing Date)	Yes No					
below and, insofar as the States application in the acknowledge the duty to	it under Title 35, United States of esubject matter of each of the climanner provided by the first particular disclose material information a between the filing date of the provided by the p	laims of this application is na ragraph of Title 35, United 5 defined in Title 37, Code of	ot disclosed in the prior United States Code, Sec. 112, I of Federal Regulations, Sec.					
(Serial No.)	(Filing Date)	(Status)						
(Serial No.)	(Filing Date)	(Status)						





I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application, to file a corresponding international application, to prosecute and transact all business in the Patent and Trademark office connected therewith:

David G. Pursel, Reg. No. 28,659; Ralph R. Veseli, Reg. No. 33,807; Bruce R. Hopenfeld, Reg. No. 39,714; Sandeep Jaggi, Reg. No. 43,331; Gary Edward Ross, Reg. No. 29,431; Kenneth D'Alessandro, Reg. No. 29,144; David B. Ritchie, Reg. No. 31,562; Marc S. Hanish, Reg. No. 42,626; John P. Schaub, Reg. No. 42,125; Gerhard W. Thielman, Reg. No. 43,186; Reynaldo C. Barceló, Reg. No. 42,290; Lee M. Pederson, Reg. No. 38,269; and Nathan R. Rieth, Registration No. 44,302.

Please direct all telephone calls to Bruce R. Hopenfeld at (408) 433-8708 and all correspondence to:

LSI Logic Corporation
Patent Law Department
1551 McCarthy Boulevard, M/S D-106
Milpitas, CA 95035

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that wilful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Wherefore, I pray that Letters Patent be granted to me for the invention or discovery described and claimed in the foregoing specification and claims, and I do hereby subscribe my name to the foregoing specification and claims, declaration, and power of attorney.

First Inventor's	Full Name	e: Chintan Desai			
Inventor's signa	ature:	em lam	<u> </u>	Date:	12116199
Residence:	Sunnyv	ale, California			
Citizenship:	India				
Post Office Add	dress:	1049 W. Olive Avenue, #4, Suni	nyvale, Califor	rnia 940	086